

ABSTRACT OF THE DISCLOSURE

An image processing device includes a peripheral circuit and an AFE device. The peripheral circuit is coupled to a display card, and the display signals may be inputted to the AFE device, which processes the display
5 signals, via the peripheral circuit. The image signals outputted from the display card are single-ended analog signals including a red signal, a green signal and a blue signal. The AFE device receives the signals and then utilizes its red, green and blue converters to convert the signals into digital
10 ones. It is to be noted that the red, green, and blue converters share the same ground, which is electrically connected to another ground of the peripheral circuit. Thus, the peripheral circuit and the AFE device have the same reference ground level so as to avoid the distortion caused when the image signals are converted from single-ended ones into the differential ones.

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